EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	1	(((module core) same test$3) and (bit with control with (positive negative) with clock) and (flip adj flop with clock with control with invert$3) and ((multiplex$3 slect$3) with control with bit)).clm.	US-PGPUB	OR	ON	2007/02/02 15:09
L4	1	(((module core) same test$3) and (bit with control with (positive negative) with clock) and ("xor" with clock with generat$3 with control)).clm.	US-PGPUB	OR	ON	2007/02/02 15:07
L3	1	(((module core) same test$3) and (bit with control with (positive negative) with clock)).clm.	US-PGPUB	OR	ON	2007/02/02 15:06
L2	8	L1 and (((SOC "system on a chip" system-on-a-chip) same (module core) same test$3) and ((scan near enable) same clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 14:58
S76	5345	(714/724,726,729 326/16,96).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 14:49
S75	4	S74 and (((SOC "system on a chip" system-on-a-chip) same (module core) same test$3) and ((scan near enable) same clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 14:49
L1	5479	(714/724,726,729 326/16,96).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 14:49
S77	6	S76 and (((SOC "system on a chip" system-on-a-chip) same (module core) same test$3) and ((scan near enable) same clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 17:16

EAST Search History

S74	5107	(714/724,726,729 326/16,96).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/25 17:15
S5	13	(((SOC "system on a chip" system-on-a-chip) same (module core) same test$3) and ((scan near enable) same clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:06
S71	137	326/16.ccls. and scan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 14:03
S73	132	326/96.ccls. and ((phase edge) near5 (signal clock enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 11:35
S72	180	326/96.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 11:34
S56	253	((clock near3 polarity) near5 control$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 10:38
S70	0	S56 and (test$3).clm. and (scan near enabl$3).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:28
S69	0	S56 and ((different multiple plurality) near10 (clock near4 domain))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:28

EAST Search History

S68	0	S56 and ((different multiple plurality) with (clock near4 domain))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:26
S67	0	S65 and ((different multiple plurality) with (clock near4 domain))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:25
S66	3	S65 and (SOC "system on a chip" system-on-a-chip system-on-chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:24
S65	207	S56 same ((phase clock polarity) near3 control$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:23
S60	0	(SOC "system on a chip" system-on-a-chip system-on-chip) and Bhawmik-S$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:23
S57	108	S56 same ((phase clock polarity) near3 control$4) and (register ROM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:23
S59	3	(SOC "system on a chip" system-on-a-chip system-on-chip) and S57	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:22
S64	14	Bhawmik-S$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:18

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S63	33	Bhaw$-S$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:18
S62	0	(SOC "system on a chip" system-on-a-chip system-on-chip) and Bhaw$-S$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:18
S61	0	(SOC "system on a chip" system-on-a-chip system-on-chip) and $-Sudipta.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:17
S29	75	(SOC "system on a chip" system-on-a-chip system-on-chip) and S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:15
S58	105	S57 and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:14
S55	108	S48 same ((phase clock polarity) near3 control$4) and (register ROM)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:14
S54	105	S48 same ((phase clock polarity) near3 control$4) and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:05
S53	207	S48 same ((phase clock polarity) near3 control$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:05

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S52	253	S48 same (phase clock polarity)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:04
S51	253	S48 and (phase clcok polarity)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:04
S50	0	S48 and S49	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:03
S49	207	((edge near2 control) near5 enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:03
S48	253	((clock near3 polarity) near5 control$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:02
S47	17	S46 and (controller with (phase clock polarity)) and domain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 17:01
S46	132	(test$3 and "scan enable").clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 16:47
S45	131034	test$3.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 16:47

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S44	23	((program$5 near3 register) and (generat$3 near3 (clock enable (control near signal)))) same (((invert$3 positive negative) near3 clock) ((rising falling) near3 edge adj triggered))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 16:47
S43	68	((program$5 near3 register) with (generat$3 near3 (clock enable (control near signal)))) and (((invert$3 positive negative) near3 clock) ((rising falling) near3 edge adj triggered))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:44
S42	2	((program$5 near3 register) with (generat$3 near3 (clock enable (control near signal)))) same (((invert$3 positive negative) near3 clock) ((rising falling) near3 edge adj triggered))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:44
S41	579	((program$5 near3 register) with (generat$3 near3 (clock enable (control near signal))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:39
S40	3844	((program$5 near3 register) with (clock enable (control near signal)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:37
S39	8478	((program$5 near3 register) same (clock enable (control near signal)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:37
S37	56	(program$5 near2 (clock enable (control near signal)) near3 generat$3) same scan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:35
S38	35	S37 not S32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:31

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S35	75	S34 not S32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:31
S34	96	((program$5 configur$3) near2 (clock enable (control near signal)) near3 generat$3) same scan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:30
S36	2	(SOC "system on a chip" system-on-a-chip system-on-chip) and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:24
S33	0	S32 not S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:22
S32	21	((programmable configurable) near2 (clock enable (control near signal)) near3 generat$3) same scan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:22
S31	21	(programmable near2 (clock enable (control near signal)) near3 generat$3) same scan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:21
S30	1491	(programmable near2 (clock enable (control near signal)) near3 generat$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:44
S28	1416	(programmable near2 (clock (control near signal)) near3 generat$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:43

EAST Search History

S27	2248	(programmable near2 signal near3 generat$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:40
S23	2	(program$5 with clock with generat$3) same ((negative postive) near3 edge) and enable and scan	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:39
S26	14	S21 not S23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:28
S21	16	(program$5 with clock with generat$3) same ((negative postive) near3 edge) and enable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:28
S25	1	"5826067".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:26
S24	1	"5960191".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:26
S22	0	(program$5 with clock with generat$3) same ((negative postive) near3 edge) and scan adj enable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:23
S20	23	(program$5 with clock with generat$3) same ((negative postive) near3 edge)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:23
S19	1	"5783960".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:14
S18	1	"5850150".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:14
S17	1	"6145105".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:11
S16	1	"6173428".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:11

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S15	1	"6442722".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:07
S14	1	"6446230".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:07
S13	1	"6473727".PN.	USPAT; USOCR	OR	ON	2006/04/12 14:06
S12	1	"6408412".PN.	USPAT; USOCR	OR	ON	2006/04/12 13:02
S11	1	"20020019962".PN.	US-PGPUB	OR	ON	2006/04/12 13:00
S10	1	"20020194560".PN.	US-PGPUB	OR	ON	2006/04/12 12:59
S9	1	"20040158789".PN.	US-PGPUB	OR	ON	2006/04/12 12:58
S8	1	"20050055615".PN.	US-PGPUB	OR	ON	2006/04/12 12:58
S7	0	S6 not S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:52
S6	10	S5 not S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:52
S4	10	S3 not S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:52
S3	13	(((SOC "system on a chip" system-on-a-chip) same core) and ((scan near enable) same clock) and test$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:51
S2	3	((SOC "system on a chip" system-on-a-chip) and (IP adj core) and ((scan near enable) same clock) and test$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:38
S1	2	"20050091562"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:13

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	1	(((module core) same test$3) and (bit with control with (positive negative) with clock) and (flip adj flop with clock with control with invert$3) and ((multiplex$3 slect$3) with control with bit)).clm.	US-PGPUB	OR	ON	2007/02/02 15:09
L4	1	(((module core) same test$3) and (bit with control with (positive negative) with clock) and ("xor" with clock with generat$3 with control)).clm.	US-PGPUB	OR	ON	2007/02/02 15:07

Interference Search History